



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Eric M. Dowling  
 App. No. : 10/074,705  
 Filed : February 13, 2002  
 For : **EMBEDDED-DRAM-DSP  
 ARCHITECTURE**  
 Examiner : Huisman, David J.  
 Group : 2183




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PATENT, TRADEMARK OFFICE

I hereby certify that this paper is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated below and is addressed to Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

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By   
 Robert F. Gazdzinski  
 Reg. No. 39,990

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**AMENDMENT AND RESPONSE TO FINAL OFFICE ACTION**  
**(INCLUDING REQUEST FOR CONTINUED EXAMINATION)**

10 Mail Stop RCE  
 Commissioner for Patents  
 P.O. Box 1450  
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Dear Sir:

In response to the Office Action dated July 25, 2005 ("Office Action"), the following is provided:

11/28/2005 SSITHIB1 00000015 501423 10074705  
 04 FC:1251 120.00 DA